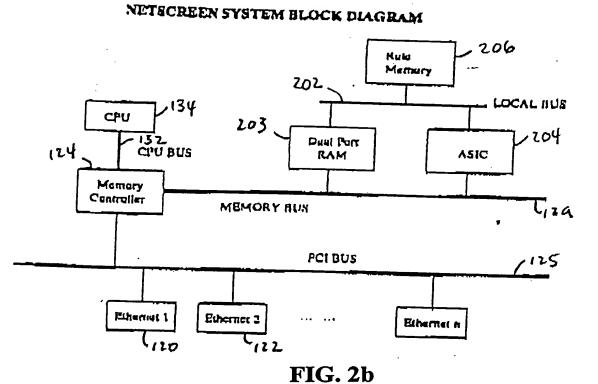


Fig.



K 110 DATAFLOW EXAMPLE: WITHOUT LOCAL BUS 130 Rule Memory 134 126 CPU .128 RAM ASIC 132 CPU BUS Memair Controller MEMORY BUS .1 1125 PCI MUS Echermee n Introduct | Ethurnat 2 122 120

FIG. 2a



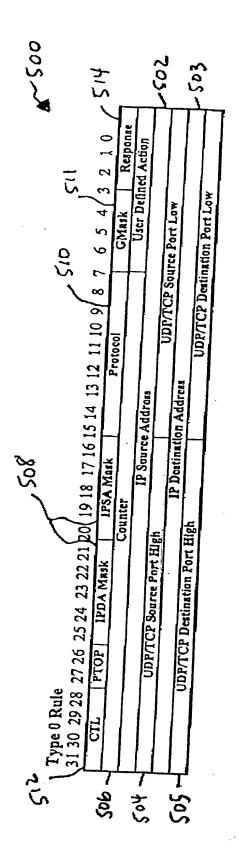


FIG.

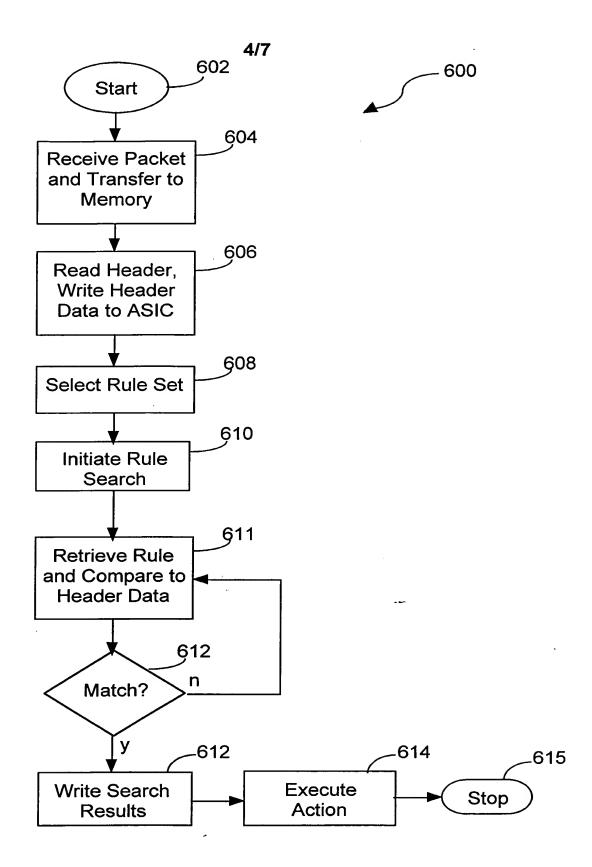


FIG. 4

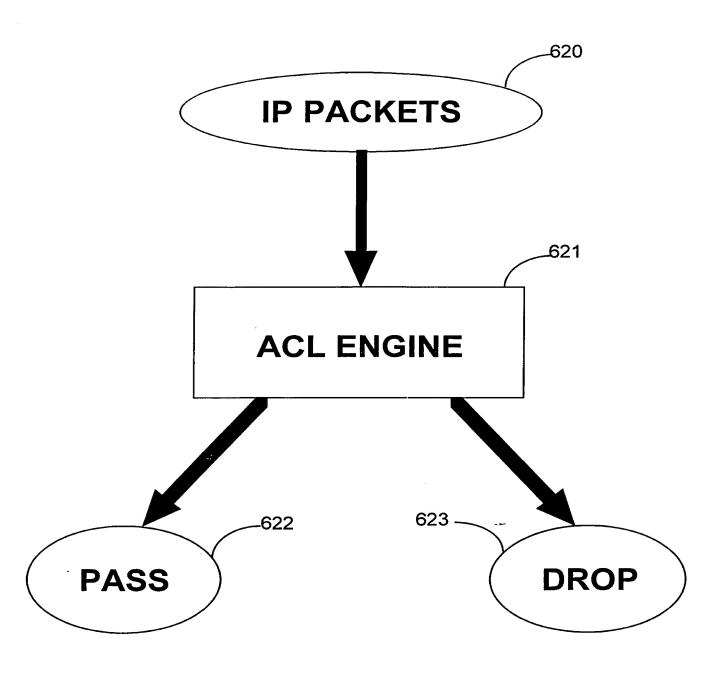


FIG. 5 (Prior Art)

